

# Channel Processing Duty Cycle

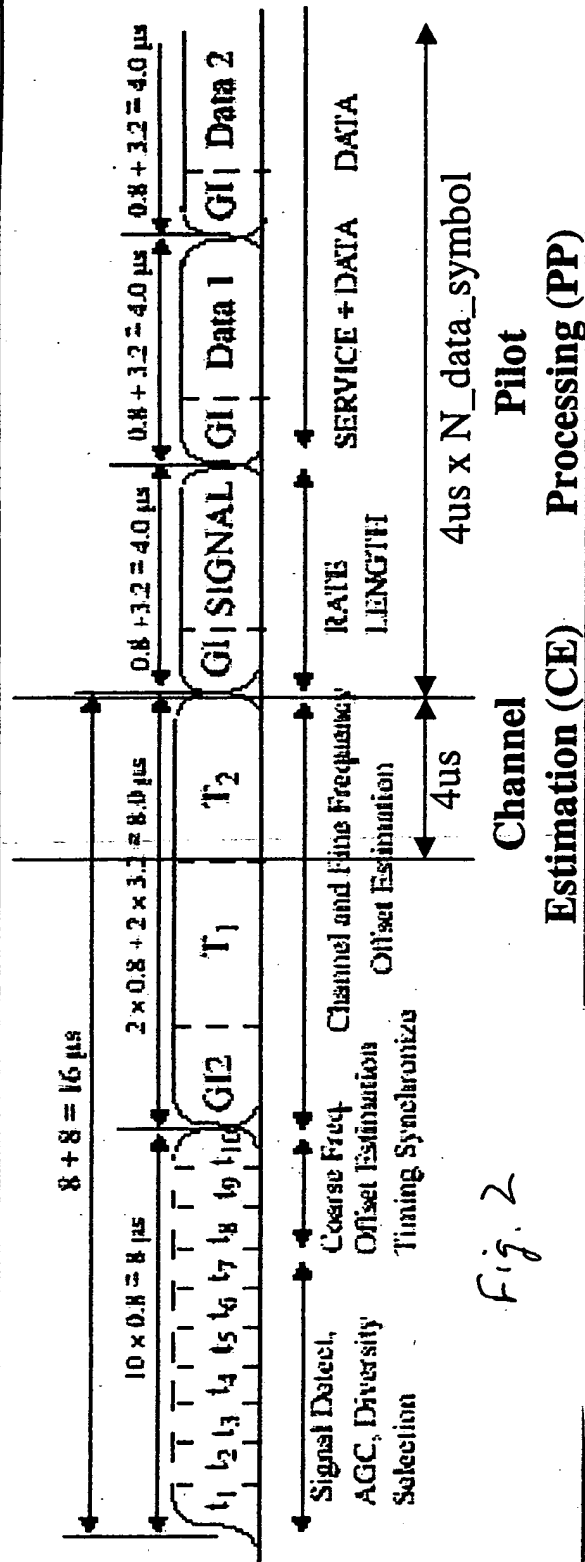


Fig. 2

Figure 3

